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D.Opens

SHEET 1 OF 1

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	APPLICANT Integrated Device Technology, Inc.	
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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
OKO	5,302,552	04/12/1994	Duchateau et al.	437	200	03/26/1993
OKO	5,728,625	03/17/1998	Tung	438	586	04/04/1996
OKO	5,736,461	04/07/1998	Berti et al.	438	651	09/11/1996
OKO	5,911,114	06/08/1999	Naem	438	684	03/21/1997
OKO	5,918,145	06/29/1999	Rodder	438	622	06/07/1995
OKO	6,033,978	03/07/2000	Fujii et al.	438	621	11/21/1996
OKO	6,037,204	03/14/2000	Chang et al.	438	231	08/07/1998
OKO	6,063,704	05/16/2000	Demirlioglu	438	664	08/02/1999
OKO	6,100,191	08/08/2000	Lin et al.	438	661	04/14/1998

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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						<input type="checkbox"/>	<input type="checkbox"/>

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

OKO	"A 2.9 μm^2 Embedded SRAM Cell with Co-Salicide Direct-Strap Technology for 0.18 μm High Performance CMOS Logic", Noda et al., 1997 IEEE.

EXAMINER <i>Douglas W. Opens</i>	DATE CONSIDERED <i>10/20/01</i>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.